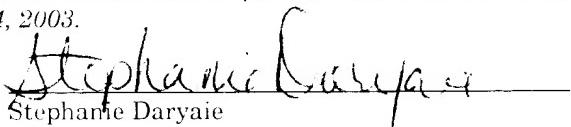




PATENT  
RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2878

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 14, 2003.

  
Stephanie Daryaei

Appl No. : 09/835,937 Confirmation No. 5612  
Applicant : Jan S. Iwanczyk, et al.  
Filed : April 16, 2001  
Title : JUNCTION-SIDE ILLUMINATED SILICON DETECTOR ARRAYS  
  
TC/A.U. : 2873  
Examiner : Constantine Hannaher  
  
Docket No. : 41876/RAG/P590  
  
Customer No. : 23363

AMENDMENT AFTER FINAL ACTION

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
August 14, 2003

Commissioner:

In response to the Office action of June 25, 2003, please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the list of claims which begins on page 3 of this paper.

**Amendments to the Drawings** begin on page 11 of this paper. Redlined drawings as well as a set of formal drawings are attached hereto as an Appendix.

**Appln No. 09/835,937**

**Amdt date August 14, 2003**

**Reply to Office action of June 25, 2003**

**Remarks/Arguments** begin on page 12 of this paper.

An **Appendix** including amended drawings figures is attached following page 14 of this paper.